

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Katsuya FURUE

Confirmation Number: 3280

Application No.: 10/751,522

Group Art Unit: 2816

Allowed: January 25, 2005

Filed: January 06, 2004

Examiner: WELLS, Kenneth B.

For: SEMICONDUCTOR INTEGRATED CIRCUIT

## LETTER RE-SUBMITTING FORMAL DRAWINGS

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability dated January 25, 2005, we are resubmitting the attached ten (10) sheets of Formal Drawings in connection with the above referenced application.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

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Facsimile: 202.756.8087 Date: March 22, 2005

Please recognize our Customer No. 20277 as our correspondence address.